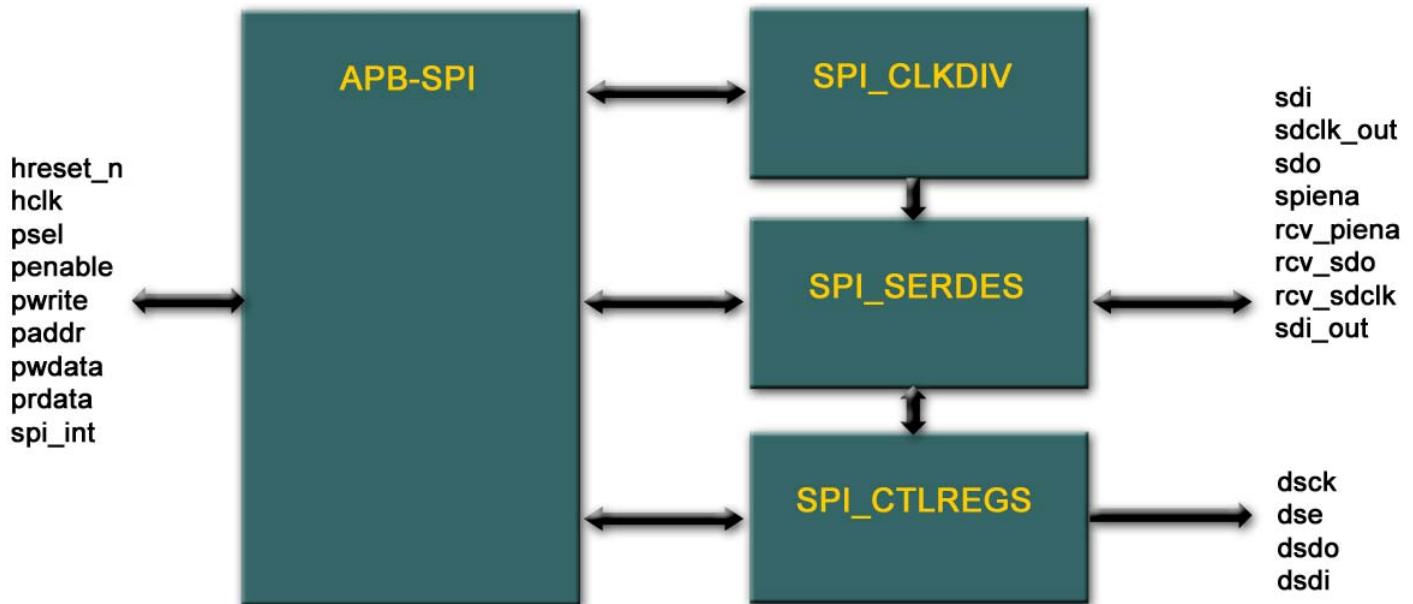


The Intrinsix SPI Silicon IP module is a gate efficient implementation of the Serial Peripheral Interface with capabilities both as a Master and Slave device. The SPI Interface is a valuable component of the **Intrinsix AMBA SoC Platform**.



## Intrinsix SPI Module capabilities:

- Fully Synchronous Design
- Popular AMBA APB Bus slave provides the interface to the host processor
- Single interrupt support to the host processor
- Programmable 16 bit clock divider allows higher bus operating speeds
- Programmable Clock Phase and Active level
- Supports the MICROWIRE™ interface
- Supports 8/16/24/32 bit transfers
- Programmable Chip Select Active level
- Unlimited Octet per transfer capability
- Short transfer detection.
- Programmed I/O Option supported
- Available as RTL in industry standard formats allowing simple synthesis to any required technology

For more information regarding the **Intrinsix AMBA SoC Platform** ask for the Intrinsix Document "G-SoCIP-AMBA" available from the Intrinsix Sales Team or online at [www.intrinsix.com/intrinsix-ip/](http://www.intrinsix.com/intrinsix-ip/)