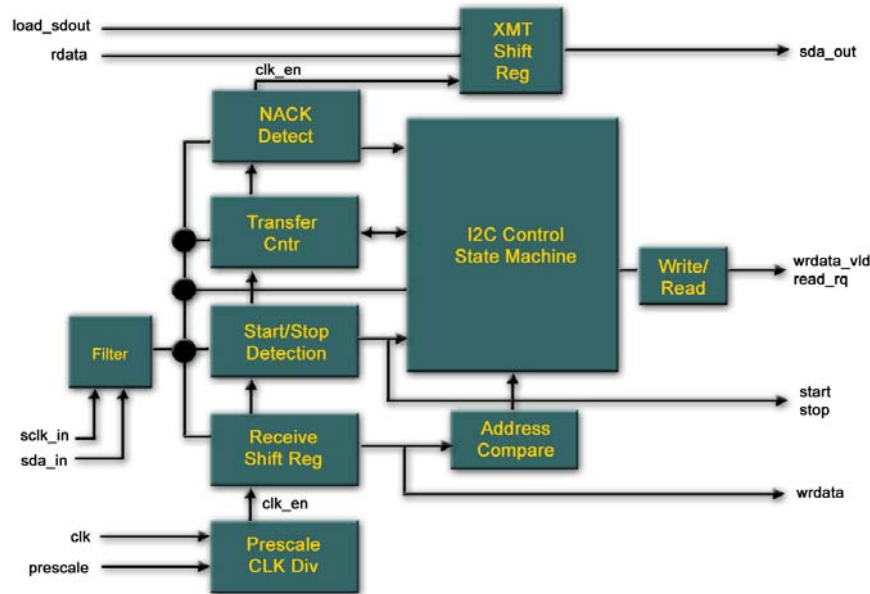
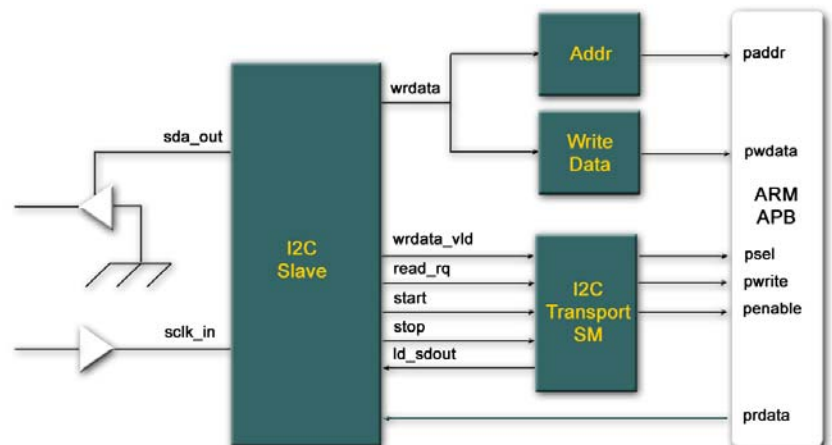


The Intrinsix I2C Slave Interface Silicon IP module is a gate efficient implementation of the Philips I2C, v2.1 specification. The host port is a generic 8-bit interface that is readily adapted to industry standard bus architectures. The I2C Slave Interface is a valuable component of the **Intrinsix AMBA SoC Platform**.



I2C Slave Interface Module capabilities:

- Implements Philips Fast Mode (0 - 400kbps) I2C specification v2.1
- Fully synchronous design, supports full scan
- Built in 16-bit clock pre-scalar
- Built in digital filtering for glitch rejection per Philips I2C specification v2.1
- Available as RTL in industry standard formats.
- 1,000 gates using 0.130µm TSMC
- 108 Slices on Xilinx Spartan 3 FPGA



Block diagram of possible implementation of an I2C to APB Bridge employing the Intrinsix I2C Slave design. The Intrinsix I2C Slave implementation provides enough external state to allow interpretation of the data arriving via I2C in any convenient order.

For more information regarding the **Intrinsix AMBA SoC Platform** ask for the Intrinsix Document "G-SoCIP-AMBA" available from the Intrinsix Sales Team or online at www.intrinsix.com/intrinsix-ip/